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Applicant HETHERTON, Scott et al	

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INTERNATIONAL SEARCH REPORT

International Application No
PCT/US 00/25118

A. CLASSIFICATION OF SUBJECT MATTER

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According to International Patent Classification (IPC) or to both national classification and IPC

B. FIELDS SEARCHED

Minimum documentation searched (classification system followed by classification symbols)
IPC 7 H01C H01G

Documentation searched other than minimum documentation to the extent that such documents are included in the fields searched

Electronic data base consulted during the international search (name of data base and, where practical, search terms used)

EPO-Internal, PAJ, WPI Data

C. DOCUMENTS CONSIDERED TO BE RELEVANT

Category *	Citation of document, with indication, where appropriate, of the relevant passages	Relevant to claim No.
X	WO 99 45551 A (BOURNS MULTIFUSE HONG KONG LTD) 10 September 1999 (1999-09-10) the whole document	1-20
P, X	EP 0 955 643 A (MATSUSHITA ELECTRIC IND CO LTD) 10 November 1999 (1999-11-10) the whole document	1-20
X	& WO 98 29879 A (MATSUSHITA ELECTRIC IND CO LTD) 9 July 1998 (1998-07-09)	1-20

☐ Further documents are listed in the continuation of box C.

☒ Patent family members are listed in annex.

* Special categories of cited documents :

A document defining the general state of the art which is not considered to be of particular relevance

E earlier document but published on or after the international filing date

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O document referring to an oral disclosure, use, exhibition or other means

P document published prior to the international filing date but later than the priority date claimed

T later document published after the international filing date or priority date and not in conflict with the application but cited to understand the principle or theory underlying the invention

X document of particular relevance; the claimed invention cannot be considered novel or cannot be considered to involve an inventive step when the document is taken alone

Y document of particular relevance; the claimed invention cannot be considered to involve an inventive step when the document is combined with one or more other such documents, such combination being obvious to a person skilled in the art.

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Date of the actual completion of the international search

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INTERNATIONAL SEARCH REPORT

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Patent document cited in search report	Publication date	Patent family member(s)	Publication date
WO 9945551 A	10-09-1999	US 6172591 B EP 1060481 A US 2001000658 A	09-01-2001 20-12-2000 03-05-2001
EP 0955643 A	10-11-1999	US 6188308 B WO 9829879 A	13-02-2001 09-07-1998

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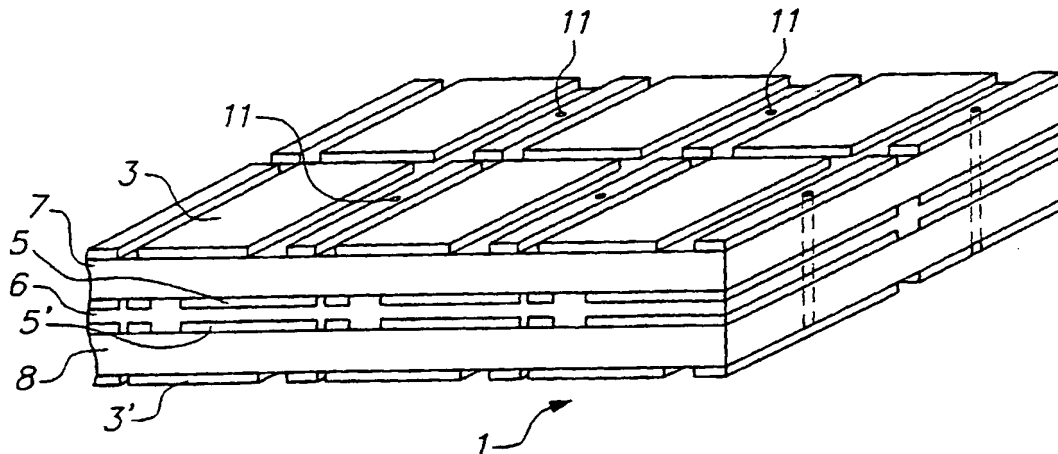
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(54) Title: **ELECTRICAL DEVICES AND PROCESS FOR MAKING SUCH DEVICES**



(57) Abstract: A process for manufacturing a composite polymeric circuit protection device in which a polymeric assembly is provided and is then subdivided into individual devices (2). The assembly is made by providing first and second laminates (7, 8), each of which includes a laminar polymer element having at least one conductive surface, providing a pattern on at least one of the conductive surfaces on one laminate, securing the laminates in a stack (1), in a desired configuration, at least one conductive surface of at least one of the laminates forming an external conductive surface (3) of the stack, and making a plurality of electrical connections (31, 51) between a conductive surface of the first laminate and a conductive surface of the second laminate. The laminar polymer elements may be PTC conductive polymer compositions, so that the individual devices made by the process exhibit PTC behavior.

WO 01/20619 A2

ELECTRICAL DEVICES AND PROCESS FOR MAKING SUCH DEVICES

BACKGROUND OF THE INVENTION

5 Field of the Invention

This invention relates to electrical devices and assemblies and methods of making such devices and assemblies.

10 Introduction to the Invention

Circuit protection devices comprising a conductive polymer composition having a positive temperature coefficient (PTC) are well-known. Such devices which are intended for surface mounting onto a substrate, e.g. a printed circuit board, are disclosed in U.S. Patents Nos. 5,831,510 (Zhang et al), 5,852,397 (Chan et al), and 5,864,281 (Zhang et al),
15 and International Publications Nos. 94/01876 (Raychem Corporation) and 95/08176 (Raychem Corporation), the disclosures of which are incorporated herein by reference. Such circuit protection devices generally comprise first and second laminar electrodes; a laminar PTC resistive element sandwiched between the electrodes; a third (residual)
20 laminar conductive member which is secured to the same face of the PTC element as the second electrode but is separated therefrom; and a cross-conductor which passes through an aperture in the PTC element and connects the third conductive member and the first electrode. This permits connection to both electrodes from the same side of the device, so that the device can be connected flat on a printed circuit board, with the first electrode on
25 top, without any need for leads. The resistive element preferably comprises a laminar element composed of a PTC conductive polymer. Preferably the device comprises an additional conductive member and an additional cross-conductor, so that the device is symmetrical and can be placed either way up on a circuit board.

30 When two of these devices are physically secured together in a stacked configuration, a composite device can be formed. Such composite devices have the same small "foot-print" on the board, i.e. occupy a small area, as a single device, but they have a lower resistance than can be conveniently produced by using a single device. In addition, the power dissipation of such a composite device is not substantially different
35 from the power dissipation of one of the devices alone. As a result, the composite device

has a lower resistance for a given hold current, where "hold current" is the largest current which can be passed through a device without causing it to trip.

SUMMARY OF THE INVENTION

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As described in International Patent Publication No. WO99/53505 (Raychem Corporation, published October 21, 1999), the disclosure of which is incorporated herein by reference, composite devices can be prepared by sorting individual devices and then assembling the sorted devices into composite devices. Such a process can be tedious, as it may require that the resistance of each individual device be read. We have now found, in accordance with the present invention, that it is possible to prepare a multilayer assembly from which individual composite devices can be divided. Such an assembly allows preparation of a large number of composite devices simultaneously. Furthermore, because the process described herein allows the patterning of individual layers of the assembly before or after fabrication into the assembly, a variety of different devices can be prepared from the same starting layers. In addition, the composition of the layers can be easily varied, allowing the simple build-up of devices with combined functionality. Various interconnection schemes between layers can be simply implemented, and devices with multiple external electrical contacts can be made without changing the basic manufacturing process. All of these further add to the broad range of different devices which can be inexpensively mass-produced by the process disclosed herein.

This invention provides methods and processes for which various operative steps can be carried out on an assembly which will yield a plurality of devices when subdivided into composite devices by subdividing along both x- and y-directions (where x and y correspond to directions in the plane of the laminar PTC elements). The ability to prepare devices in this way is a significant improvement over other methods, for example that described in International Patent Publication No. WO99/53505, because for this invention, individual devices do not need to be individually assembled, increasing the efficiency and therefore reducing the cost of the manufacturing process. Finally, the method of combining layers of materials to form composite devices disclosed herein allows an extremely simple yet adaptable method for forming a variety of devices without the necessity of changing the basic manufacturing process.

35

In a first aspect this invention provides a process for manufacturing a composite polymeric circuit protection device, said process comprising

- (1) providing a polymeric assembly comprising
- 5 (a) providing first and second laminates, each of which comprises a laminar polymer element having at least one conductive surface,
- (b) providing a pattern of conductive material on at least one of the conductive surfaces on one laminate;
- 10 (c) securing the laminates in a stack in a desired configuration, at least one conductive surface of at least one of the laminates comprising an external conductive surface of the stack, and
- 15 (d) making a plurality of electrical connections between a conductive surface of the first laminate and a conductive surface of the second laminate; and
- (2) subdividing the stack into individual devices each of which comprises at least one electrical connection.
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In a second aspect this invention provides a polymeric assembly comprising:

- 25 (a) a first laminate comprising a laminar polymer element having at least one conductive surface having a pattern;
- (b) a second laminate comprising a laminar polymer element having at least one conductive surface having a pattern, said second laminate being secured to the first laminate in a stack so that the stack has first and second external conductive surfaces; and
- 30 (c) a plurality of transverse conductive members which run through the first and second laminates between the first and second external conductive surfaces.

35 Using either the process or the assembly of the invention, devices can be made by creating electrode precursors in the form of conductive surfaces of appropriate shapes

upon resistive elements which are larger than the desired final shape, forming a stack of a plurality of resistive elements which is also larger than the desired final shape, and then subdividing the stack into individual devices. Electrodes of appropriate shapes can be made by removing unwanted portions of any one, or any combination, of the conductive surfaces. The removal can be accomplished by milling, stamping, or etching, for example. Alternatively, the electrode precursors can be formed by patterning conductive material onto any one or any combination of the PTC resistive element surfaces by chemical vapor deposition, electrodeposition, sputtering, etc. Conductive material may also be applied to the faces of the PTC resistive elements by use of an adhesive or tie layer. Electrical interconnection between a desired combination of the conductive surfaces of the plurality of resistive elements can be accomplished before the stack has been subdivided into individual devices. Alternatively, some or all of the electrical connections between desired electrodes or contact points can be made after the stack has been subdivided into composite devices. The electrical interconnection can be designed so that connection is made between some of the conductive surfaces of the stack or the electrodes of the device, but not all.

Thus in a third aspect, this invention provides a composite device, which can be made, for example, using the process of the first aspect of the invention or the assembly of the second aspect, comprising

- (1) first and second external laminar electrodes,
 - (2) third and fourth internal laminar electrodes,
 - (3) first and second laminar PTC resistive elements, each of which (i) exhibits PTC behavior, and (ii) comprises a laminar element composed of a PTC conductive polymer,
- said first resistive element having a first face to which the first external electrode is secured and an opposite second face to which the third internal electrode is secured, and said second resistive element having a first face to which the second external electrode is secured and an opposite second face to which the fourth internal electrode is secured,

- (4) a fifth external laminar conductive member which is (i) secured to the first face of the first PTC resistive element, and (ii) is spaced apart from the first external electrode,
- 5 (5) a sixth external laminar conductive member which (i) is secured to the first face of the second PTC resistive element, and (ii) is spaced apart from the second external electrode,
- 10 (6) a seventh internal laminar conductive member which (i) is secured to the second face of the first PTC resistive element, and (ii) is spaced apart from the third internal electrode,
- 15 (7) an eighth internal laminar conductive member which (i) is secured to the first face of the second PTC resistive element, and (ii) is spaced apart from the fourth internal electrode,
- 20 (8) a first aperture which runs between the first external electrode of the first laminar PTC element and the second external electrode of the second laminar PTC element,
- (9) a second aperture which runs between the fifth external laminar conductive member of the first laminar PTC element and the sixth external laminar conductive member of the second laminar PTC element,
- 25 (10) a first transverse conductive member which
- (a) lies within the first aperture,
- 30 (b) runs between the first external electrode of the first laminar PTC element and the second external electrode of the second laminar PTC element,
- (c) is secured to the first PTC element, the second PTC element and the third laminar element, and
- 35

(d) is physically and electrically connected to the first external laminar electrode, the seventh internal laminar conductive member, the eighth internal laminar conductive member, and the second external laminar electrode, but is not connected to the third or the fourth internal electrode, and

(11) a second transverse conductive member which

(a) lies within the second aperture,

(b) runs between the fifth external laminar conductive member and the sixth external laminar conductive member,

(c) is secured to the first PTC element, the second PTC element and the third laminar polymer layer, and

(d) is physically and electrically connected to the fifth external laminar conductive member, the third internal electrode, the fourth internal electrode, and the sixth external laminar conductive member, but is not connected to the first or second external electrode.

BRIEF DESCRIPTION OF THE DRAWINGS

The invention is illustrated in the accompanying drawings, in which Figure 1 is a perspective view of a section of a stack formed in the first aspect of the invention, which can be subdivided into a plurality of individual composite devices;

Figure 2 is an exploded view of a stack which has been patterned on the internal conductive surfaces;

Figure 3 is a plan view of the top of a section of a stack;

Figure 4 is a cross-sectional view of a section of a stack along line IV-IV in Figure 3;

Figure 5 is a perspective view of a composite device of the invention;

Figure 6 is a cross-sectional view of a composite device mounted on a printed circuit board parallel to the board;

5 Figure 7 is a plan view of composite devices further illustrated in Figures 8, 9, 10, 11, 14, and 21;

Figures 8, 9, and 10 are cross-sectional views along line VIII-VIII in Figure 7 of composite devices with two elements connected in parallel;

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Figure 11 is a cross-sectional view along line VIII-VIII in Figure 7 of a composite device with three elements connected in parallel;

Figure 12 is a plan view of another composite device with two elements connected in parallel but with no residual conductive members as illustrated in Figure 13;

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Figure 13 is a cross-sectional view along line XIII-XIII in Figure 12;

Figure 14 is a cross-sectional view along line VIII-VIII of Figure 7 of another composite device with two elements connected in parallel;

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Figure 15 is a cross-sectional view of a composite device with two elements connected in series;

Figure 16 is a plan view of a composite device with more than two external electrical connection points;

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Figure 17 is an electrical diagram of the interconnection scheme of the individual devices connected together to form the composite devices of Figures 16 and 18 to 20.

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Figures 18, 19, and 20 are cross-sectional views along lines XVIII-XVIII, XIX-XIX, and XX-XX, respectively, in Figure 16;

Figure 21 is a cross-sectional view of a composite device with two external electrodes and one internal electrode;

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Figure 22 is a plan view of a composite device with multiple electrical connections between layers of a composite device;

Figure 23 is a cross-sectional view along line XXIII-XXIII in Figure 22;

Figures 24 and 26 are exploded views of stacks of the invention which form an assembly and can be subdivided into a plurality of individual composite devices; and

Figures 25 and 27 are perspective views of devices of the invention made from the stacks of Figures 24 and 26, respectively.

DETAILED DESCRIPTION OF THE INVENTION

As described and claimed below, and as illustrated in the Figures, this invention incorporates a number of features. Where such a feature is disclosed in a certain context or as part of a particular combination, it can also be used in other contexts and in other combinations, including combinations of any number of such features.

PTC and Resistive Elements

Assemblies and devices of the invention generally comprise at least one laminar polymer element or resistive element which comprises a PTC composition which exhibits positive temperature coefficient (PTC) behavior, i.e. it shows a sharp increase in resistivity with temperature over a relatively small temperature range. The term "PTC" is used to mean a composition or device that has an R_{14} value of at least 2.5 and/or an R_{100} value of at least 10, and it is preferred that the composition or device should have an R_{30} value of at least 6, where R_{14} is the ratio of the resistivities at the end and the beginning of a 14°C range, R_{100} is the ratio of the resistivities at the end and the beginning of a 100°C range, and R_{30} is the ratio of the resistivities at the end and the beginning of a 30°C range.

The PTC compositions used in the present invention are preferably conductive polymers which comprise a crystalline polymer component and, dispersed in the polymer component, a particulate filler component which comprises a conductive filler, e.g. carbon black or a metal. The filler component may also contain a non-conductive filler, which changes not only the electrical properties of the conductive polymer but also its physical properties. The composition can also contain one or more other components, e.g. an

antioxidant, crosslinking agent, coupling agent, flame retardant, or elastomer. The PTC composition preferably has a resistivity at 23°C of less than 50 ohm-cm, particularly less than 10 ohm-cm, especially less than 5 ohm-cm. Suitable conductive polymers for use in this invention are disclosed for example in U.S. Patents Nos. 4,237,441 (van

5 Konynenburg et al), 4,304,987 (van Konynenburg), 4,514,620 (Cheng et al), 4,534,889 (van Konynenburg et al), 4,545,926 (Fouts et al), 4,724,417 (Au et al), 4,774,024 (Deep et al), 4,935,156 (van Konynenburg et al), 5,049,850 (Evans et al), 5,378,407 (Chandler et al), 5,451,919 (Chu et al), 5,582,770 (Chu et al), 5,747,147 (Wartenberg et al), and 5,801,612 (Chandler et al), and U.S. Patent Application No. 09/364,504 (Isozaki et al,
10 filed July 30, 1999). The disclosure of each of these patents and applications is incorporated herein by reference.

Alternatively, the PTC composition can be a ceramic material.

15 Laminar Elements

Devices of the invention preferably comprise PTC resistive elements which are laminar elements, and can be composed of one or more conductive polymer members, at least one of which is composed of a PTC material. When there is more than one
20 conductive polymer member, the current preferably flows sequentially through the different compositions, as for example when each composition is in the form of a layer which extends across the whole device. When there is a single PTC composition, and the desired thickness of the PTC element is greater than that which can conveniently be prepared in a single step, a PTC element of the desired thickness can conveniently be
25 prepared by joining together, e.g. laminating by means of heat and pressure, two or more layers, e.g. melt-extruded layers, of the PTC composition. When there is more than one PTC composition, the PTC element will usually be prepared by joining together, e.g. laminating by means of heat and pressure, elements of the different compositions.

30 Assemblies of the invention comprise first and second laminates, and may comprise additional laminates. The first and second laminates each comprise a laminar polymer element having at least one conductive surface, e.g. in the form of a metal foil electrode as described below. In this specification, each laminate is referred to as a layer. The laminar elements of the first and second laminates may comprise PTC compositions
35 which are the same, or the layers may comprise different PTC compositions. For example, PTC compositions of different resistivities may be used, and an interconnection

scheme devised so that one layer can act as a heater, and a second layer can act as an overcurrent protection device. The layers may also comprise PTC compositions of different switching temperatures (i.e. the temperature at which the device switches from a low resistance to a high resistance state). For example, such a device may be useful for creating a two-tiered PTC temperature sensor, with one layer being the most sensitive for a lower temperature range, and the second layer being the most sensitive for a higher temperature range. Furthermore, one or more of the laminates may comprise a zero temperature coefficient of resistance (ZTC) composition or a negative temperature coefficient of resistance (NTC) composition.

It is not necessary that each of the laminates comprise a conductive layer. For example, other compositions which can be used for a laminar element in the composite devices comprise a dielectric material such as polyester or a filled dielectric material such as FR4 epoxy. This can function as an insulating layer which provides extra rigidity to the device, or the material can be chosen to assist in the mounting and packaging of the device. In addition, a laminar element can comprise a composition which has a relatively high thermal conductivity to aid in the thermal transfer between layers of the composite, or between a substrate and a layer of the composite for a surface mounted device. Conversely, a laminar element can comprise a composition which has a relatively low thermal conductivity to act as a thermal insulator between layers or between a layer and the substrate. When it is desired that the device have the capability to respond to an overvoltage, a layer of the composite can comprise a material which is normally insulating but becomes conducting when a certain voltage threshold level is reached. Such compositions include varistor particles dispersed in a polymeric matrix. Other compositions which may be useful for various embodiments of this invention include flame retardant materials, intumescants, and microwave absorbing materials to allow the heating of the device using radiation of a specific frequency range.

The thicknesses of the laminar elements comprising the assembly used to prepare a composite device may be different. For example, a very thin laminar element may be used as one layer to provide an extremely low resistance, and a thicker laminar element may be used as a second layer to provide mechanical strength.

Electrodes and Conductive Surfaces

Particularly useful devices made by the process of the invention comprise at least two metal foil electrodes, with polymer elements sandwiched between them. An especially useful device will comprise a stack comprising n polymeric PTC elements, each having two metal foil electrodes, and $(n-1)$ adhesive layers sandwiched between the PTC elements in an alternating pattern to form a composite device, with the PTC elements comprising the top and bottom components of the stack. This device will have the electrodes electrically connected such that the PTC elements will be connected in parallel, resulting in a composite device which has a low resistance at 20°C, generally less than 10 ohms, preferably less than 5 ohms, more preferably less than 1 ohm, particularly less than 0.5 ohm, with yet lower resistance being possible, e.g. less than 0.05 ohm. Particularly suitable foil electrodes are microrough metal foil electrodes, in particular as disclosed in U.S. Patents Nos. 4,689,475 (Matthiesen) and 4,800,253 (Kleiner et al), and in International Patent Publication No. WO95/34081 (Raychem Corporation, published December 14, 1995), the disclosure of each of which is incorporated herein by reference. The electrodes can be modified so as to produce desired thermal effects and so as to provide electrical contact points for various interconnection points between the layers of the composite device to give the desired functionality, and to provide electrical contact points for mounting the device onto printed circuit boards, sockets, clips, or other suitable applications. Examples of composite devices which incorporate multiple internal and external contact points are illustrated in Figures 16 to 20, 22, and 23.

Similar types of metal foil can be used to form the conductive surfaces of the laminates in the polymeric assembly. Alternatively, the conductive surface may be formed from a conductive ink, a sputtered or otherwise applied metal layer, a metal mesh, or another suitable layer. Particularly preferred conductive surfaces are those which can be etched, e.g. for patterning, and/or soldered easily. The conductive surface of the laminates has a resistivity at 25°C which is at least 100 times lower than the resistivity at 25°C of the polymer element to which it is attached.

The patterns may be the same on both sides of a given laminate, or they may be different. Additional patterns may be created at any point in the process, e.g. on an external conductive surface of the laminate once a stacked assembly is formed, or as attachments are made to internal conductive surfaces before the stacked assembly is completed.

Apertures and Cross-Conductors

5 The term "aperture" is used herein to denote an opening which, when viewed at right angles to the plane of the device,

(a) has a closed cross-section, e.g. a circle, an oval, or a generally polygonal shape, or

10 (b) has a reentrant cross-section, the term "reentrant cross-section" being used to denote an open cross-section which (i) has a depth at least 0.15 times, preferably at least 0.5 times, particularly at least 1.2 times, the maximum width of the cross-section, e.g. a quarter circle or a half circle or an open-ended slot, and/or (ii) has at least one part where the opposite edges of the cross-section are parallel to each other.

15

Since the invention involves assemblies which can be divided into a plurality of electrical devices, the apertures will normally be of closed cross-section, but if one or more of the lines of division passes through an aperture of closed cross-section, then the apertures in the resulting devices will have open cross-sections. Although for some
20 embodiments, it is desirable that an open cross-section is a reentrant cross-section as defined above, in order to ensure that the cross-conductor, which passes through the aperture, is not damaged or dislodged during installation or use of the device, for other embodiments, it is preferred that the cross-conductor is a plating on one transverse flat face of the device. In order to produce such a device, it is preferred that the assembly,
25 which is to be divided into a plurality of devices, has a plurality of elongate rectangular apertures, e.g. slots, each with metal plating thereon. The assembly is then divided so that each plated aperture provides a flat transverse conductive member on a number of devices.

30 The apertures in the assembly may be of different sizes and/or shapes to accommodate device configurations and current-carrying capabilities.

The aperture can be a circular hole, and this is satisfactory in many cases. However, if the assembly includes apertures which are traversed by at least one line of
35 division, elongate apertures may be preferred because they require less accuracy in the lines of division.

When the aperture is not traversed by a line of division, it can be as small as is convenient for a cross-conductor having the necessary current-carrying capacity. Generally a single cross-conductor is all that is needed to make an electrical connection to the first electrode to the opposite side of the device. However, two or more cross-conductors can be used to make the same connection. The number and size of the cross-conductors, and, therefore, their thermal capacity, can have an effect on the rate at which a circuit protection device will trip. Generally the apertures and cross-conductors can extend throughout all layers of the assembly. Alternatively, apertures and cross-conductors can extend through only some layers of the assembly to form devices of different functionality.

The aperture can be formed before the cross-conductor is put into place, or the formation of the aperture and the placing of the cross-conductor can be carried out simultaneously. A preferred procedure is to form an aperture, e.g. by drilling, slicing, routing or any other appropriate technique, and then to plate or otherwise coat or fill the interior surface of the aperture. The plating can be effected by electroless plating, or electrolytic plating, or by a combination of both. The plating can be a single layer or multiple layers, and can be composed of a single metal or a mixture of metals, in particular a solder. The plating will often be formed on other exposed conductive surfaces of the assembly. If such plating is not desired, then the other exposed conductive surfaces can be masked or otherwise desensitized, or undesired plating can be selectively removed. The invention includes the possibility that the plating will produce not only the cross-conductor but also at least part of the laminar conductive members in the device.

The plating techniques which are used for making conductive vias through insulating circuit boards can be used in the present invention.

Another technique for providing the cross-conductors is to place a moldable or liquid conductive composition in preformed apertures, and if desired or necessary, to treat the composition, while it is in the apertures, so as to produce cross-conductors of desired properties. The composition can be supplied selectively to the apertures, e.g. by means of a screen, or to the whole assembly, if desired after pretreating at least some of the assembly so that the composition does not stick to it. For example, a molten conductive composition, e.g. solder, could be used in this way, if desired, using wave soldering techniques.

The cross-conductor can also be provided by a preformed member, e.g. a metal rod or tube such as a rivet. When such a preformed member is used, it can create the aperture as it is put in place in the device.

5 The cross-conductor can partially or completely fill the aperture. When the aperture is partially filled, it can be further filled (including completely filled) during the process in which the device is connected to other electrical components, particularly by a soldering process. This can be encouraged by providing additional solder in and around
10 the aperture, especially by including a plating of solder in and around the aperture. Normally at least a part of the cross-conductor will be put in place before the device is connected to other electrical components. However, the invention includes the possibility that the cross-conductor is formed during such a connection process, as for example by the capillary action of solder during a soldering process.

15 A cross-conductor may be designed such that it electrically connects some of the layers, i.e. some of the laminates, together, but not all. Such cross-conductors are depicted in Figure 15. A method for making such a cross-conductor would comprise forming an aperture which is larger than the desired size for the cross-conductor, filling
20 the aperture with an insulating substance, forming an inner aperture within the insulating substance, and plating the inner aperture to render it conductive. This method insulates the internal electrodes from the cross-conductor, yet allows the external electrodes to be electrically connected.

25 Connectors which are not Cross-Conductors

The electrical connection(s) between the electrode(s) and any residual member(s) on the various faces of the PTC resistive elements is preferably through a cross-conductor as described above. However, it can be of any kind, for example a connector which will
30 remain in place even if it is not bonded to the other parts of the device, for example a U-shaped member which extends around the ends of a layer or combination of layers of the device.

Residual Laminar Conductive Members

A preferred embodiment of a device of the invention comprises an additional (residual) conductive member which is secured to the same face of the PTC element as the second electrode, but is separated therefrom. This residual laminar conductive member which, with the cross conductor or other connector, can be present to provide an electrical path to other electrodes, is formed by removing part of a laminar conductive member, the remainder of the of the laminar conductive member then being an electrode. Residual laminar conductive members may be present on both internal and external faces of the laminar elements. The shape of the residual laminar conductive member, and the shape of the gap between the residual member and an electrode, can be varied to suit the desired characteristics of the device and for ease of manufacture. The residual conductive member is conveniently a small rectangle at one end of a rectangular device, separated from an electrode by a rectangular gap. Alternatively, the residual member can be an island separated from the electrode by a gap of closed cross-section. Devices can also be designed without a residual laminar conductive member, as shown in Figures 12 and 13.

Additional Laminar Elements

The first and second laminar PTC resistive elements of the device or the first and second laminates of the assembly can be physically secured together in a stack, using a third laminar element between them. The third laminar element may comprise an electrically nonconductive adhesive, e.g. a hot-melt adhesive or a curable bonding material, to which fillers can be added to achieve particular thermal or mechanical properties. The third laminar element can also comprise curable monomeric organic or inorganic systems, such as epoxies, acrylates, allyls, urethanes, phenolics, esters, alkyds, etc. If it is desired that the laminar element act as an electrical insulator, then it is preferred that the resistivity is at least 10^6 ohm-cm, particularly at least 10^9 ohm-cm. For some embodiments it may be desired that the third laminar element comprise a conductive material. For these embodiments, the third laminar element serves to connect the layers together electrically as well as physically. A composite device configuration incorporating a conductive third laminar element is illustrated in Figure 15. For other embodiments, it may be desired that the third element comprise a conductive material which is electrically conductive in only one direction (see Figure 14). The third laminar element can also provide other functions, e.g. a thermally conductive layer to facilitate thermal transfer between the layers of the composite device.

Alternatively, devices can be designed which do not include a separate laminar layer to secure the elements of the device together. For example, a device can be made which is similar to that depicted in Figure 8, with the laminar element 26 eliminated. The cross-conductors 32 and 52 can be relied upon to both electrically connect the layers together in parallel, and to physically secure the layers together. Figure 21 depicts another example where the composite device does not require a separate laminar element between the layers.

Devices

In a simple device, as shown in Figure 5 there are two external electrodes, two internal electrodes, two cross-conductors or other connectors, and four residual conductive members. This configuration is useful since the device will then be symmetrical top to bottom, allowing for ease of installation by automated equipment or otherwise.

Particularly preferred circuit protection devices of the invention have a resistance at 23°C of less than 1 ohm, preferably less than 0.5 ohm, particularly less than 0.3 ohm, especially less than 0.1 ohm, and comprise first and second laminar PTC resistive elements, each of which (a) is composed of a conductive polymer which has a resistivity at 23°C of less than 50 ohm-cm, preferably less than 10 ohm-cm, particularly less than 5 ohm-cm, and which exhibits PTC behavior, and (b) has a first face and a second face. A first external metal foil electrode contacts the first face of the first PTC element and a second external metal foil electrode contacts the first face of the second PTC element. Third and fourth internal metal foil electrodes contact the second faces of the first and second PTC elements, respectively. The device preferably has a fifth and sixth residual external metal foil conductive members, the fifth contacting the first face of the first PTC element and spaced apart from the first external electrode, and the sixth contacting the first face of the second PTC element and spaced apart from the second external electrode. Generally there are seventh and eighth residual internal metal foil conductive members, the seventh contacting the second face of the first PTC element and spaced apart from the third internal electrode, and the eighth contacting the second face of the second PTC element and spaced apart from the fourth internal electrode. The device may also comprise one or more additional laminar polymer elements, which may be conductive or insulating. Preferably one of the additional elements is a third laminar polymer element

which is insulating, lies between the first and the second PTC elements, and is secured to the exposed internal surfaces of the PTC elements which may comprise the internal faces of the PTC elements or their internal electrodes or internal conductive members. The PTC elements, the electrodes and the residual conductive members define two apertures, the first of which runs between the first external electrode, the seventh and eighth internal residual conductive members, and the second external electrode, and the second of which runs between the fifth residual external conductive member, the third and fourth internal electrodes and the sixth residual external conductive member, and through the first and second PTC elements and the third laminar polymer layer, if present. In addition, the device comprises first and second transverse conductive members which are composed of metal. The first transverse conductive member lies within the first aperture, and is physically and electrically connected to the first and second external electrodes and the seventh and eighth internal residual conductive members. The second transverse conductive member lies within the second aperture, and is physically and electrically connected to the fifth and sixth external residual conductive members and the third and fourth internal electrodes.

Other embodiments of the device may not contain residual (also called additional) conductive members.

The devices of the invention can be of any appropriate size. However, it is an important advantage for applications to make the devices as small as possible. Preferred devices have a maximum dimension of at most 12 mm, preferably at most 7 mm, and/or a surface area of at most 60 mm², preferably at most 40 mm², especially at most 30 mm². The surface area can be much smaller, e.g. at most 15 mm².

Processes

The methods disclosed herein make it possible to prepare devices very economically by carrying out all or most of the process steps on a large stack of laminates, and then dividing the laminate into a plurality of individual composite devices. The division of the stack can be carried out along lines which pass through any, some or all of the conductive surfaces, or through any, some or all of the cross conductors. These lines of division, also called isolation lines or delineation lines, can be of any shape suitable for producing devices of a particular configuration, e.g. straight, curved, or at angles. Similarly "functional" lines, e.g. the gaps between an electrode and a residual

member, can also be of any suitable shape. The process steps prior to the division can in general be carried out in any convenient sequence. For example, it is often convenient to pattern the internal conductive surfaces prior to assembling the stack, and to pattern the external conductive surfaces after assembly. However, it is possible to pattern both internal and external conductive surfaces prior to assembly. The patterning of a conductive surface can be the same as or different from that on other conductive surfaces in the stack, depending on the desired functionality of the final device. For example, Figures 5, 6, 8, 9, 11 and 12 show devices which have internal electrodes which are the mirror images of external electrodes. Figures 10 and 18 to 20 show devices which have internal electrodes which are patterned differently than external electrodes. Often, it is useful to pattern conductive surfaces by removing, e.g. by etching, stamping, or milling, conductive material. Alternatively, the pattern can be produced by an additive process, e.g. screen printing, sputtering, or deposition. For some applications, it is useful to remove strips of conductive material in staggered strips alternately from opposite sides of a laminate layer to balance the physical stresses in the product. The resulting pattern will contain gaps or recesses suitable for separating a second electrode from a residual member in a device, separating one device from another, providing delineation for subdividing the assembly into individual devices, allowing for orientation of individual laminates or the assembled stack, or providing marking.

Cross-conductors, i.e. electrical connections, can be formed before or after the laminates are formed into the stack. If it is desired to form cross-conductors which do not traverse all layers of the stack, it may be convenient to form them for the desired laminate layers only, and then assemble the stack. Alternatively, using a blind via process, the connection can be made after the stack is assembled. Assembly of the stack can be accomplished in stages, for example, some laminates are prepared and secured together, some further processing steps are performed on the partially constructed stack (e.g. formation and plating of cross-conductors), and then other laminates can be secured to this partially constructed stack to complete the assembly. Subdivision of the stack into composite devices can be accomplished using a variety of techniques such as sawing, shearing, dicing, punching, and snapping, e.g. by using a saw, a shear, a blade, a wire, a waterjet, a snapping device, a laser, or a combination of these. Some preferred processes for making devices from a single laminate are disclosed in U.S. Patent No. 5,864,281. These processes may be adapted for the subdivision of a stack of laminates such as that described herein. Alternatively, some of the steps in the process, e.g. securing the

laminates in a stack and making a plurality of electrical connections by means of cross-conductors, can be accomplished simultaneously.

5 In order to minimize curling or warping due to shrinkage during subsequent processing steps, it may be preferred to apply a pattern around the perimeter of at least one laminate. The preferred pattern is produced by a process comprising selectively removing conductive material from at least one conductive surface of each laminate in an alternating cross-directional design, e.g. in the shape of a "W" or a "Z", around the perimeter of the laminate such that there is electrical continuity between the outer edge of
10 the conductive surface for the external layers.

Part or all of the external conductive surface may be covered with an insulating layer, e.g. a solder mask or marking material such as that disclosed in U.S. Patent No. 5,831,510.

15 The invention is illustrated in the accompanying drawings, in which features such as apertures and the thicknesses of components are not drawn to scale to make them more clear. Figure 1 is a perspective view of a section of a stack 1 which has two laminar elements 7 and 8, each of which has patterned external conductive surfaces 3 and 3', respectively, and patterned internal conductive surfaces 5 and 5', respectively. The
20 elements 7 and 8 are secured to each other with insulating laminar element 6. Tubular cross conductors 11 extend through the stack as shown.

Figure 2 is an exploded view of a stack under construction according to the
25 process of the invention. Two laminar elements 7 and 8, each having patterned internal conductive surfaces 5 and 5', respectively, and unpatterned external conductive surfaces 3 and 3', respectively, are included in the stack, with a laminar element 6 sandwiched between 7 and 8. Registration holes 4 are used to orient the elements of the stack uniquely, align them relative to each other, and to position the stack for subsequent
30 processes such as patterning of external surfaces and the formation of apertures.

Figure 3 is a plan view of a patterned external conductive surface 3 of a section of a stack. C marks where the subdivision of the stack into composite devices will occur. Figure 4 is a cross-sectional view along line IV-IV of Figure 3. The stack includes laminar elements 7 and 8 each having internal conductive surfaces 5 and 5' and external
35 conductive surfaces 3 and 3', and laminar element 6 which is sandwiched between 7 and

8. The stack has been plated to provide the tubular cross conductors 11 in each of the apertures (and a plating 12 on other exposed external surfaces of the stack). The stack as shown will be subdivided through the tubular cross conductors, forming cross conductors with semicircular cross-sections.

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Figure 5 is a perspective of a composite device 2 formed by subdividing a stack. Two laminar PTC elements 17 and 18, each having external electrodes 14 and 14', respectively, external residual conductive members 36 and 36', internal electrodes 16 and 16' and internal residual conductive members 38 and 38', are secured together with laminar element 26. A first transverse member 31 and a second transverse member 51 are hollow tubes formed by a plating process in which the exposed surfaces are plated with copper and then with solder to form a first plating 32 on transverse member 31, and a second plating 52 on transverse member 51. A dielectric coating 55 covers the external surfaces of the device except where it is desired to make electrical connection. A plating 12 is applied to the exposed portions of the external electrodes. The region between the dashed lines indicates portions under the dielectric coating 55 in which there is no electrode material.

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Figure 6 shows in cross-section a composite device 2 as in Figure 5 soldered to traces 41 and 43 on an insulating substrate 9.

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Figure 7 is a plan view for a variety of composite devices shown in cross-section along line VIII-VIII in Figures 8 to 11, 14, and 21. The dashed lines indicate portions positioned underneath the dielectric layer 55, wherein the electrode material is not present. Note that for the cross-sectional views in Figures 8-11, 14 and 21, the dielectric layer 55 is not shown. Figures 8 and 9 show two configurations for PTC elements connected in parallel. For the device shown in Figure 8 when it is in its switched, high resistance state, the laminar element 26 will have no potential drop across it when a potential is applied to one external electrode 14 and one external residual conductive member 36. However, for the device shown in Figure 9, when in its switched state, the laminar element 26 will have a potential drop across it for the same external electrical connection. Figure 10 shows a variant of the device shown in Figure 8, with no internal residual conductive members. Figure 11 shows a composite device formed by connecting three laminar elements 17, 18, and 19 in parallel, with laminar element 26 between 17 and 18, and laminar element 26' between 18 and 19. The version of the device shown has

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internal electrodes 16, 16', 16'' and 16''', and internal residual conductive members 38, 38', 38'', and 38'''.

Figure 12 is a plan view of a device with no residual conductive members; a cross-sectional view along XIII-XIII is shown in Figure 13. Dashed lines indicate regions positioned under the external dielectric layer 55 where electrode material is not present. Dielectric layer 55 is not shown in Figure 13.

Figure 14 is a composite device formed by the process of this invention, in which cross conductors do not extend completely through all layers of the stack. To make the device as shown here, cross conductors 59 extend only between external and internal conductive surfaces of each laminar element of the stack; the laminar elements are then secured together using an anisotropically conductive substance 57 which conducts only in the z-direction, where z- refers to the direction from bottom to top of the composite device. Conductive substance 57 provides electrical connection between internal residual conductive members 38 and 38' and between internal electrodes 16 and 16', without causing 38 or 38' to be shorted to 16 or 16'.

Figure 15 shows a composite device in which laminar elements 17 and 18 are connected in series. The laminar elements are secured together in the stack using a conductive material 61. Cross-conductors are made in the stack which connect to some conductive surfaces, but not all. To form such a cross conductor, apertures larger than the desired dimensions of the cross conductor are formed through the stack. The apertures are then filled with an insulating substance 63, and two smaller apertures 65 and 67 are formed within the volumes filled with the insulator 63. The apertures 65 and 67 and exposed external electrodes have platings 32 and 52.

Figure 16 shows a plan view of a composite device which includes two devices and has three external electrical connection points. A diagram of the electrical connections for the two devices 77 and 79 is shown in Figure 17.

Figure 18 shows a cross-section along line XVIII-XVIII in Figure 16. Cross-conductor 52 is in electrical contact with the internal residual members 38 and 38'. A gap separates the residual members 38 and 38' from internal electrodes 16 and 16'. Additional conductive member 46' is also present.

Figure 19 shows a cross-section along line XIX-XIX in Figure 16. Cross-conductor 72 is in electrical contact with internal residual conductive members 38 and 38'. A gap separates the residual members 38 and 38' from internal electrodes 16 and 16'. Additional conductive member 46 is also present.

Figure 20 shows the cross-sectional view along XX-XX in Figure 16.

Figure 21 illustrates a composite device which has only one internal electrode 16, formed from a stack with only one internal conductive surface. A laminar element 17 is combined with the laminar element 78. The laminar elements can be pressed together to form a bond, so that no third laminar element is required to secure the laminar elements together. For example, 17 can comprise a PTC element and 78 can comprise a dielectric substrate with adhesive properties.

Figure 22 is a plan view of a composite device which has multiple cross conductors to provide extra current carrying capacity, and extra robustness should one or both of the other cross conductors become damaged or form an open circuit. The dashed line indicates regions where there is no electrode material; the dotted circle indicates the region of an additional cross-conductor.

Figure 23 is a cross-sectional view along XXIII-XXIII in Figure 22 (dielectric layer 55 not shown). A third aperture 81 has a metal plating 82 and forms an additional electrical connection between the internal electrodes 16 and 16'. Note that there is a region around the external electrodes 14 and 14' where no electrode material is present.

Figure 24 is an exploded view of a stack 1 in which three etched conductive laminate layers 107,108,109 are attached to one another by means of nonconductive laminar layers 106',106'' in the form of adhesive layers. Additional nonconductive layers 106 and 106''' connect metal foil layers 110,111 to etched laminate layers 107 and 109, respectively. The resulting stack can be divided into individual devices 2, as shown in Figure 25. Following etching and/or other processing steps, segments of metal foil layers 110,111 remain on exposed adhesive surfaces 116,116', respectively, as pads for connection to a circuit board or other substrate. Present on each device are first transverse member 31 with first plating 32 and second transverse member 51 with second plating 52.

Figure 26 is an exploded view of a stack 1 in which a single etched and drilled conductive laminate layer 117 is sandwiched between two nonconductive laminar layers 116,116'. Each nonconductive laminar layer, which may be an adhesive, e.g. an epoxy pre-preg, may comprise one or more separate layers. Laminar metal foil layers 120,121 are attached to nonconductive laminar layers 116,116', and form the outer layers of the stack. When the layers are laminated together by means of heat and pressure, the adhesive will fill the apertures in conductive laminate 117. Following processing, individual devices 2, shown in Figure 27, can be separated from stack 1. Electrical connection pads 122, formed from metal foil layer 120, are used to attach one or more electrical components, e.g. a silicon device, to the surface of the device. Attachment of components is disclosed in International Patent Application No. PCT/US00/07081 (filed March 17, 2000), the disclosure of which is incorporated herein by reference. Electrical connection pads 123, formed from metal foil layer 121, are used to attach the device to a circuit board or other substrate. First and second transverse members 31 and 51, respectively, are plated with layers 32 and 52. An "isolated" transverse member or via 124 is also present. This is formed from an adhesive-filled aperture through which another hole is drilled and plated.

The invention is illustrated by the following Examples.

Example 1

A stack in accordance with Figures 1 and 2 was prepared by the following method. Two laminates, each having a thickness of about 0.264 mm (0.0104 inch) were prepared by attaching a nickel/copper foil having a thickness of about 0.0356 mm (0.0014 inch) to both sides of a 0.193 mm (0.0076 inch)-thick sheet of conductive polymer. The conductive polymer was prepared by mixing about 40% by volume carbon black (Raven™ 430, available from Columbian Chemicals) with about 60% by volume high density polyethylene (Chevron™ 9659, available from Chevron), and then extruding into sheet and laminating in a continuous process. The laminated sheet was cut into individual laminates of 0.30 m x 0.41 m (12 inch x 16 inch). The laminates were irradiated to 4.5 Mrad using a 4.5 MeV electron beam.

Each of the laminates was drilled in an asymmetric pattern around its the periphery to provide holes and slots to register the laminates in a known x-y orientation in the plane of the laminate. These registration holes and slots were used to align plaques

relative to each other in forming a stack, and for subsequent alignment of the tooling for imaging, solder masking, and plating operations. A 0.0762 mm (0.003 inch)-thick layer of modified acrylic adhesive (Pyrallux™ LFO, available from DuPont) was also drilled with registration holes suitable for alignment.

One surface of one foil layer of each of two laminates were patterned using an etching technique in which they were first coated with an etch resist, then imaged in a desired pattern. The etch resist was developed and etching was accomplished using cupric chloride before the resist was stripped away. These same foil layers were patterned to define the periphery of the individual devices and the residual conductive members. In addition, the outer edges of the metal foil on the laminate were etched to provide an alternating cross directional pattern around the perimeter, as shown in Figure 2. Paths providing electrical continuity were utilized during the subsequent electrolytic plating of Sn/Pb.

A stack was formed by positioning two laminates with their pattern-matched etched sides facing inward, and with an adhesive layer sandwiched in between, as shown in Figure 2. A fixture was used to align the layered laminates, and the stack was heated while under pressure to permanently attach the layers into a laminated structure. The thickness of the stack formed was approximately 0.61 mm (0.024 inch).

Holes having a diameter of 0.94 mm (0.037 inch) were drilled through the entire stack to form apertures. The stack was treated with a plasma etch. The apertures were then coated with colloidal graphite, and the stack was electrolytically plated with copper.

The external metal foil layers of the stack were then patterned by etching. The registration holes were used to ensure the pattern which was etched was properly aligned with the internal layers which were etched previously. An alternating cross-directional pattern around the edges was etched as previously described.

A soldermask (Finedel DSR 2200 C-7, available from Tamura Kaken Co., Ltd.) was applied to one external metal foil layer of the stack, tack-cured, and then applied to the second external metal foil layer of the stack, and tack-cured. The soldermask was then imaged and developed. Marks were applied to identify individual parts, and the panel was then heated to fully cure the mask. A SnPb solder plate was deposited in the solder pad regions for use in attaching devices to circuit boards.

The assembly was divided to produce devices as shown in Figure 5 by first separating the assembly into strips using a shear or saw, and then subdividing the strips into individual devices by mechanical snapping using a two-step process in which the strips were first bent to form a fracture in the conductive polymer at the isolation line, and then were sheared along that isolation line. The devices produced had dimensions of about 4.5 mm x 3.4 mm x 0.7 mm (0.179 inch x 0.133 inch x 0.029 inch), and a resistance of about 0.031 ohm. Following installation onto a printed circuit board by solder reflow, the devices had a resistance of about 0.050 ohm.

Example 2

A stack in accordance with Figure 24 was prepared by the following method. Three laminates were prepared, irradiated, and drilled with registration holes as in Example 1. Four layers of modified acrylic adhesive as in Example 1, and two layers of 1 oz. Cu foil (0.034 mm (0.00135 inch) thick) which was grey-oxide treated on one side were also drilled with registration holes suitable for alignment. Using the etching technique of Example 1, the outer surfaces of both foil electrodes on all three laminates were patterned to define the periphery of the individual devices and the residual conductive members, and to define the alternating cross directional pattern on the periphery of the panel.

A stack was formed by positioning one layer of the Cu foil on the bottom (treated side up), followed by one adhesive layer, the three laminates with their pattern-matched etched sides oriented in the fixture and with an adhesive layer sandwiched in between each layer, followed by one adhesive layer on the topmost laminate, and then one Cu foil layer placed on the top (treated side down). A fixture was used to align the layered laminates, and the stack was heated while under pressure to permanently attach the layers into a laminated structure. The thickness of the resulting stack was approximately 1.19 mm (0.047 inch). Holes having a diameter of 0.94 mm (0.037 inch) were drilled through the entire stack to form apertures. The stack was treated with a plasma etch, the apertures were coated with colloidal graphite, and the stack was electrolytically plated with copper.

The external metal foil layers of the stack were then patterned by etching using the registration holes to ensure proper alignment. An alternating cross directional pattern around the edges was etched as previously described.

Marks and solder were applied as in Example 1. The stacked assembly was then divided to produce devices as shown in Figure 25 using the procedure of Example 1. The devices produced had dimensions of about 4.5 mm x 3.4 mm x 1.2 mm (0.179 inch x 0.133 inch x 0.047 inch), and a resistance of about 0.018 ohm. Following installation onto a printed circuit board by solder reflow, the devices had a resistance of about 0.029 ohm.

Example 3

A stacked assembly in accordance with Figure 26 was prepared by the following method. One laminate having a thickness of about 0.198 mm (0.0078 inch) was prepared by attaching a nickel/copper foil having a thickness of about 0.0356 mm (0.0014 inch) to both major sides of a 0.127 mm (0.005 inch)-thick sheet of conductive polymer. The conductive polymer was prepared by mixing about 37% by volume carbon black (Raven™ 430) with about 10.5% by volume high density polyethylene (LB832, manufactured by Equistar) and about 52.5% copolymer (EBA705, manufactured by Equistar), as in Example 1. and then extruding into sheet and laminating in a continuous process. The laminated sheet was cut into individual laminates of 0.10 m x 0.41 m (4 inch x 16 inch).

The laminate was drilled with registration holes as in Example 1 and with holes having a diameter of 0.127 mm (0.050 inch) to create apertures in the laminate. Four layers of 0.038 mm (0.0015 inch)-thick layer of epoxy pre-preg (44N Multifilm, available from Arlon) and two layers of 1 oz Cu foil treated as in Example 1 were also drilled with registration holes suitable for alignment.

The outer surfaces of both foil electrodes on the laminate were patterned using an etching technique as described in Example 1, to define the residual conductive members, as well as additional etched features which acted as registration fiducial marks for subsequent isolation processes.

A stack was formed by positioning one layer of the Cu foil on the bottom (treated side up), followed by two pre-preg layers (shown as a single layer in Figure 26), the laminate layer, two pre-preg layers, and then one Cu foil layer placed on the top (treated

side down). A fixture was used to align the layers, and the stack was heated while under pressure to permanently attach the layers into a laminated structure and to force the epoxy to completely fill the apertures in the laminate. The thickness of the stack formed was approximately 0.61 mm (0.024 inch).

5 Holes having a diameter of 0.94 mm (0.037 inch) and 0.57 mm (0.023 inch) were drilled through the entire stack to form apertures, and holes having a diameter of 0.57 mm (0.023 inch) were drilled through the entire stack to form apertures which were centered on the epoxy filled aperture; this latter aperture was isolated from the laminate's electrode
10 by the epoxy. The stack was treated with a plasma etch, the apertures were coated with colloidal graphite, and the stack was electrolytically plated with copper.

The external metal foil layers of the stack were then patterned by etching. The registration holes were used to make sure the pattern which was etched was properly
15 aligned with the internal layers which were etched previously.

To produce devices as shown in Figure 27, the assembly was divided by a saw, slicing the length of a panel in one direction and then rotating the panel 90 degrees and dicing the width of the panel in one direction, using the etched fiducial features on the
20 laminate as saw locating marks. The devices produced had dimensions of about 4.5 mm x 13.77 mm x 0.61 mm (0.177 inch x 0.542 inch x 0.024 inch)

These individual devices were heat-treated on a belt furnace to heat the devices to above the polymer melting temperature ($>130^{\circ}\text{C}$), cooled to room temperature, and then
25 irradiated to 7 Mrad using a cobalt radiation source.

The resulting devices had a resistance of about 0.028 ohm when measuring across the conductive polymer laminate and $>1 \times 10^6$ ohm when measuring across the isolated via and the conductive polymer laminate. Following installation onto a printed circuit
30 board or onto Ni leads by solder reflow, the devices had a resistance of about 0.042 ohm when measuring across the conductive polymer laminate and $>1 \times 10^6$ ohm when measuring across the isolated via and the conductive polymer laminate. The devices were suitable for attachment of electrical components directly onto the device, and the apertures and transverse conductive members were positioned so that, depending on the
35 exact electrical connection, the attached electrical component, e.g. a circuit board element,

could be either electrically connected to or electrically isolated from the conductive polymer laminate.

What is claimed is:

1. A process for manufacturing a composite polymeric circuit protection device, said process comprising

- (1) providing a polymeric assembly comprising
 - (a) providing first and second laminates, each of which comprises a laminar polymer element having at least one conductive surface,
 - (b) providing a pattern of conductive material on at least one of the conductive surfaces on one laminate;
 - (c) securing the laminates in a stack in a desired configuration, at least one conductive surface of at least one of the laminates comprising an external conductive surface of the stack, and
 - (d) making a plurality of electrical connections between a conductive surface of the first laminate and a conductive surface of the second laminate; and
- (2) subdividing the stack into individual devices each of which comprises at least one electrical connection.

2. A process according to claim 1, wherein the pattern in step (b) is formed by selectively removing a portion of conductive material from at least one of the conductive surfaces on one laminate, preferably by etching, milling, or stamping.

3. A process according to claim 1, which further comprises providing a pattern of conductive material on at least one of the external conductive surfaces, preferably by selectively removing a portion of conductive material from the external conductive surface.

4. A process according to claim 3, wherein at least one of the patterned external conductive surfaces is at least partially covered with an insulating layer

5. A process according to claim 3, wherein the patterns on the internal and external conductive surfaces are different.
6. A process according to claim 1, wherein an additional conductive layer is added to at least part of at least one of the external conductive surfaces.
7. A process according to claim 1 wherein steps (c) and (d) are performed simultaneously.
8. A process according to claim 1, wherein at least one laminate is marked to provide a unique identification of orientation, preferably wherein the laminate markings provide delineation for subdividing into individual devices.
9. A process according to claim 1, wherein the assembly comprises a third laminate.
10. A process according to claim 1, wherein the laminates are secured to each other in the stack by an adhesive.
11. A process according to claim 1, wherein electrical connection is made between conductive surfaces of the first and second laminates in the stack by (i) forming an aperture which extends through the stack, and (ii) forming a conductive member within the aperture.
12. A process according to claim 1, wherein the electrical connections are positioned so that the individual device comprises at least two electrical connections.
13. A process according to claim 1, wherein the laminar polymer element in at least one of the laminates comprises a PTC conductive polymer composition.
14. A process according to claim 13, wherein (a) the laminar polymer element in each laminate comprises a PTC conductive polymer composition and (b) the PTC conductive polymer composition in each laminate is the same as or different from the PTC conductive polymer composition in the other laminates.

15. A process according to claim 1, wherein at least one of the laminar polymeric elements comprises a ZTC conductive polymeric material, an NTC conductive polymeric material, or an insulating polymeric material.

16. A process according to claim 1, wherein the individual devices are subdivided from the assembly using a saw, a shear, a blade, a wire, a waterjet, a snapping device, a laser, or a combination of these.

17. A process according to claim 1 wherein the conductive surface on each laminate comprises a metal foil.

18. A polymeric assembly comprising:

- (a) a first laminate comprising a laminar polymer element having at least one conductive surface having a pattern;
- (b) a second laminate comprising a laminar polymer element having at least one conductive surface having a pattern, said second laminate being secured to the first laminate in a stack so that the stack has first and second external conductive surfaces; and
- (c) a plurality of transverse conductive members which run through the first and second laminates between the first and second external conductive surfaces.

19. A composite device comprising

- (1) first and second external laminar electrodes,
- (2) third and fourth internal laminar electrodes,
- (3) first and second laminar PTC resistive elements, each of which (i) exhibits PTC behavior, and (ii) comprises a laminar element composed of a PTC conductive polymer,

said first resistive element having a first face to which the first external electrode is secured and an opposite second face to which the third internal electrode is secured, and said second resistive element having a first face to which the second external electrode is secured and an opposite second face to which the fourth internal electrode is secured,

- (4) a fifth external laminar conductive member which is (i) secured to the first face of the first PTC resistive element, and (ii) is spaced apart from the first external electrode,
- (5) a sixth external laminar conductive member which (i) is secured to the first face of the second PTC resistive element, and (ii) is spaced apart from the second external electrode,
- (6) a seventh internal laminar conductive member which (i) is secured to the second face of the first PTC resistive element, and (ii) is spaced apart from the third internal electrode,
- (7) an eighth internal laminar conductive member which (i) is secured to the first face of the second PTC resistive element, and (ii) is spaced apart from the fourth internal electrode,
- (8) a first aperture which runs between the first external electrode of the first laminar PTC element and the second external electrode of the second laminar PTC element,
- (9) a second aperture which runs between the fifth external laminar conductive member of the first laminar PTC element and the sixth external laminar conductive member of the second laminar PTC element,
- (10) a first transverse conductive member which
 - (a) lies within the first aperture,

- (b) runs between the first external electrode of the first laminar PTC element and the second external electrode of the second laminar PTC element,
 - (c) is secured to the first PTC element, the second PTC element and the third laminar element, and
 - (d) is physically and electrically connected to the first external laminar electrode, the seventh internal laminar conductive member, the eighth internal laminar conductive member, and the second external laminar electrode, but is not connected to the third or the fourth internal electrode, and
- (11) a second transverse conductive member which
- (a) lies within the second aperture,
 - (b) runs between the fifth external laminar conductive member and the sixth external laminar conductive member,
 - (c) is secured to the first PTC element, the second PTC element and the third laminar polymer layer, and
 - (d) is physically and electrically connected to the fifth external laminar conductive member, the third internal electrode, the fourth internal electrode, and the sixth external laminar conductive member, but is not connected to the first or second external electrode.

20. A device according to claim 19 which further comprises a third laminar element which (i) comprises an insulating polymer, (ii) is inserted between the first and second laminar PTC resistive elements, and (iii) secures the first laminar PTC element to the second PTC element.

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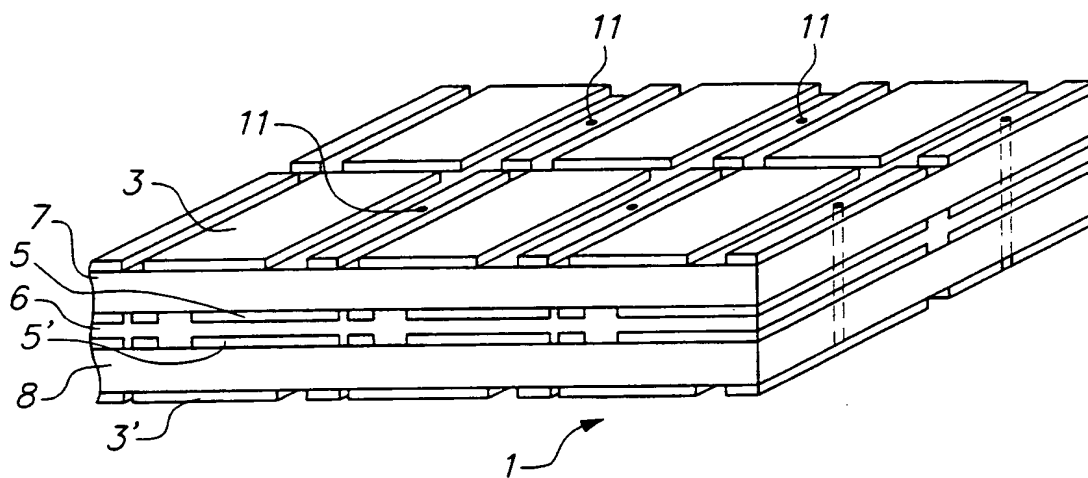


FIG. 1

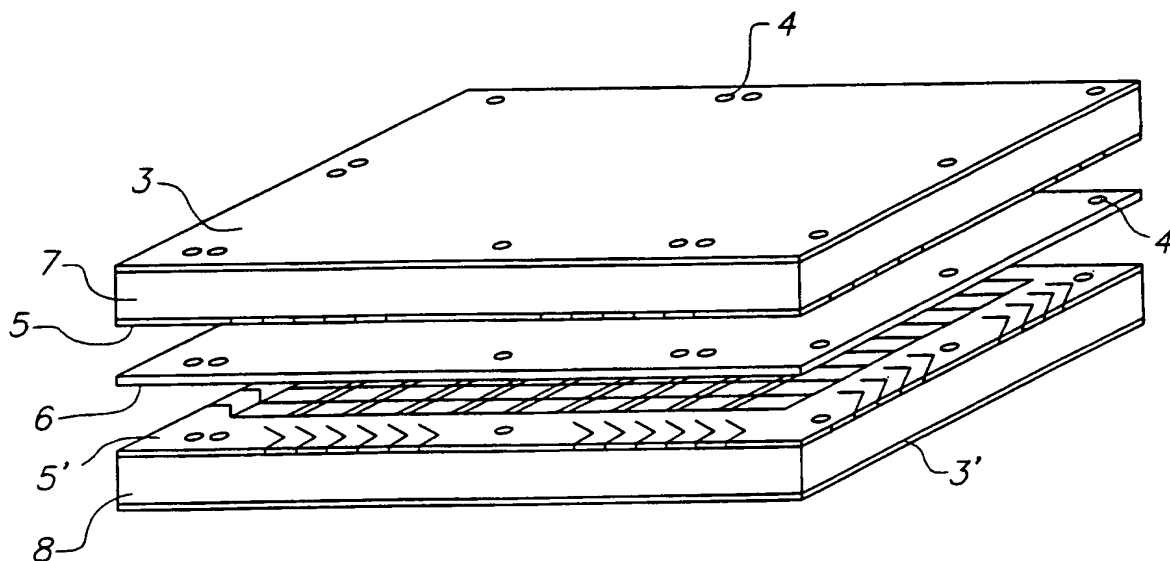


FIG. 2

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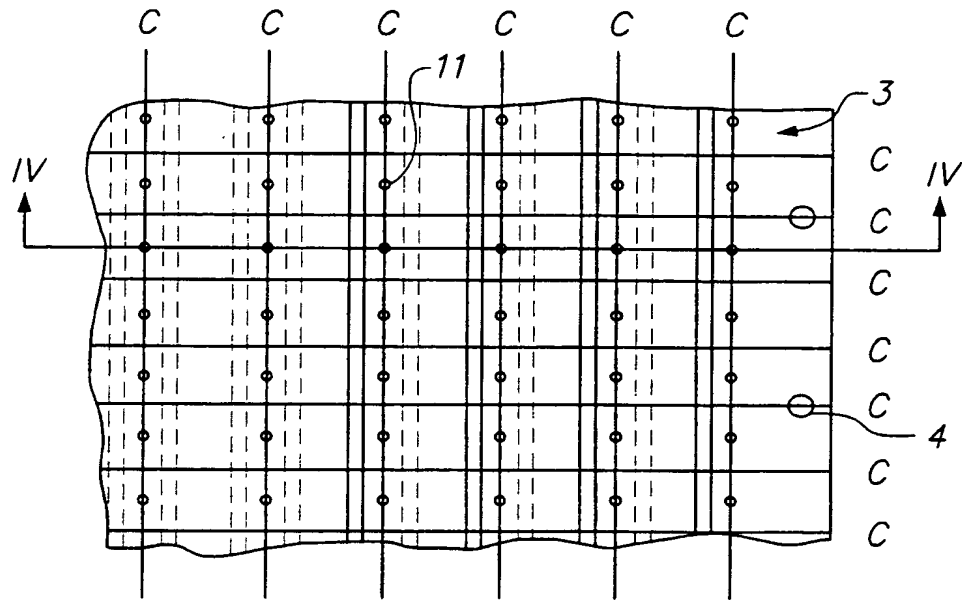


FIG. 3

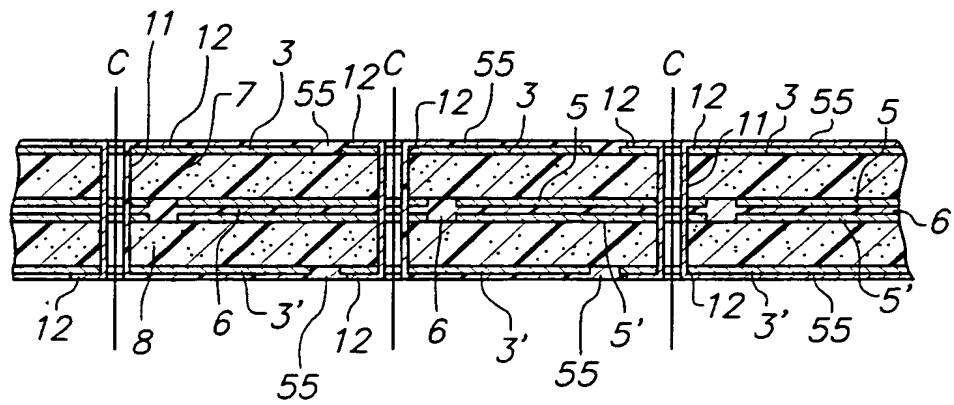


FIG. 4

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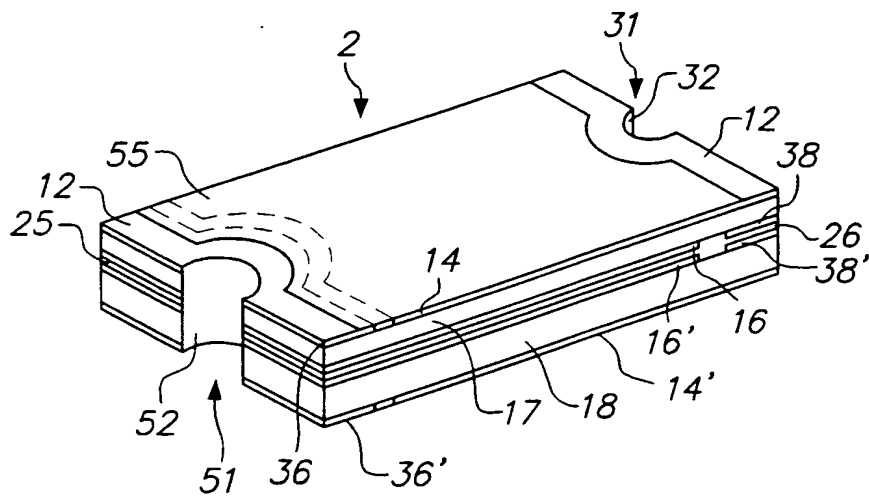


FIG. 5

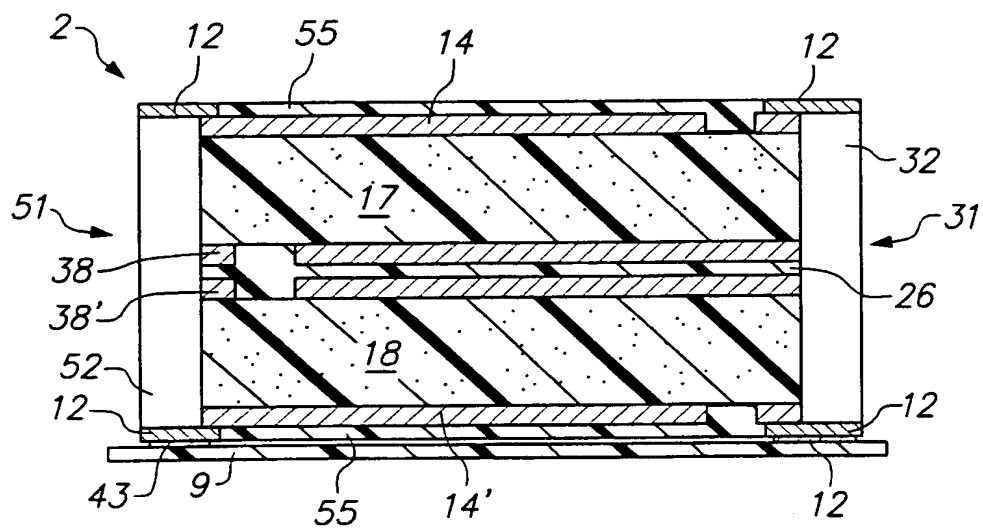


FIG. 6

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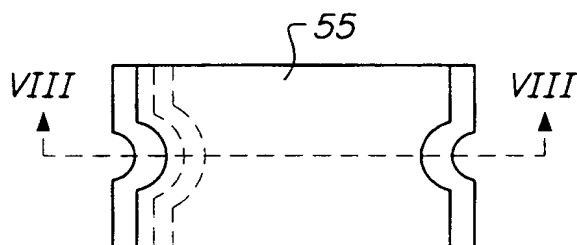


FIG. 7

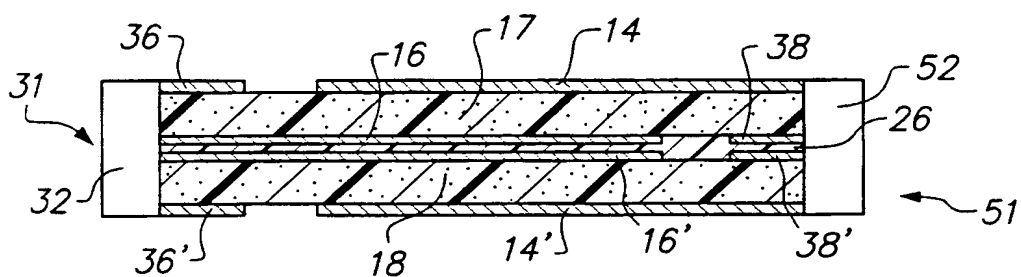


FIG. 8

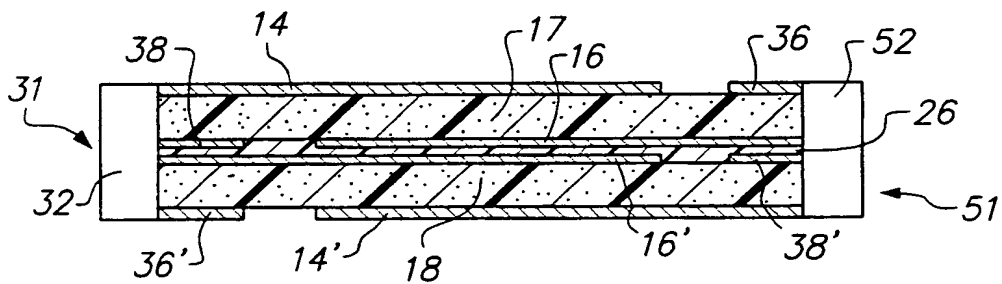


FIG. 9

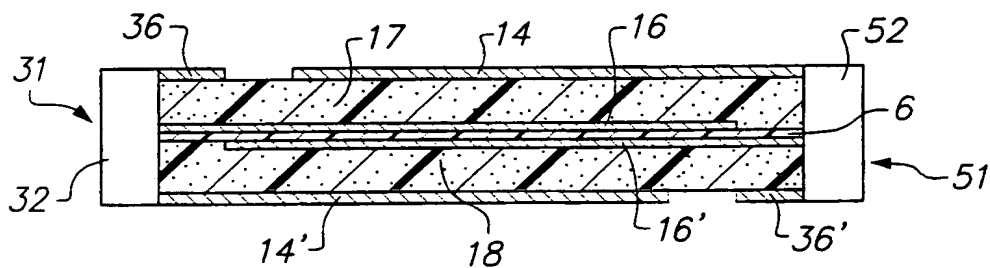
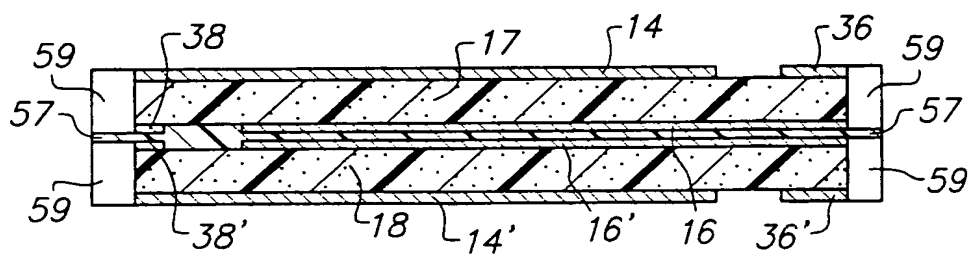
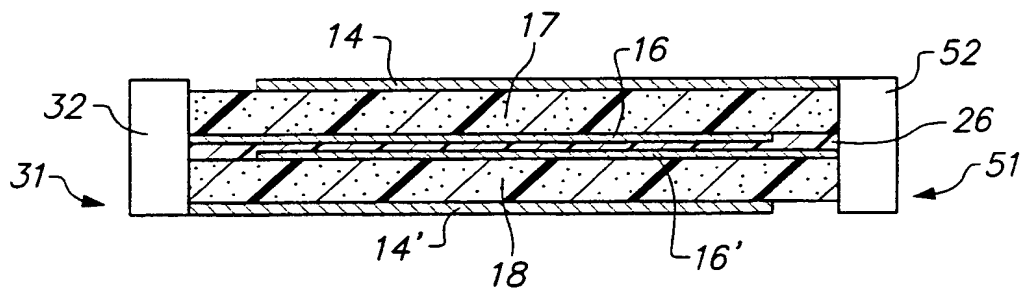
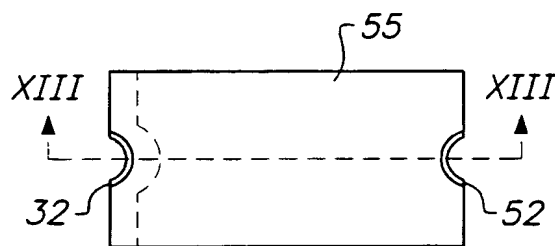
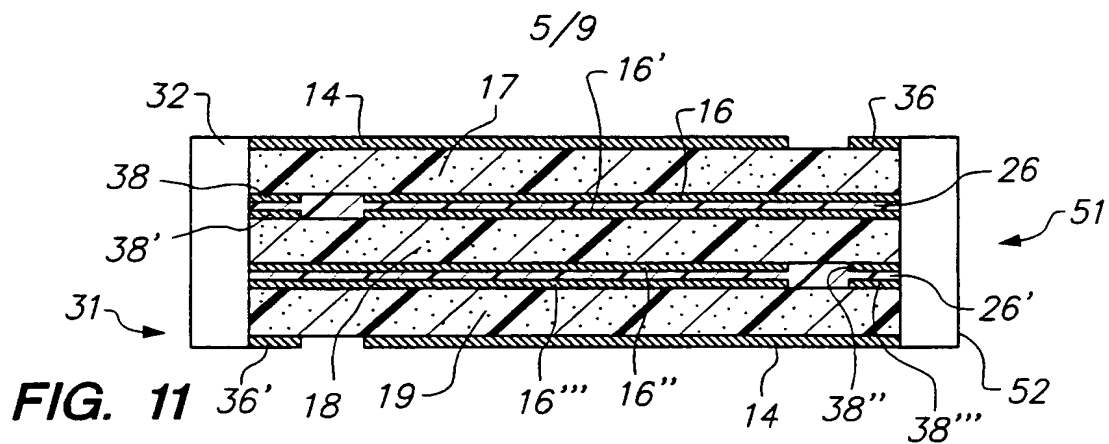


FIG. 10



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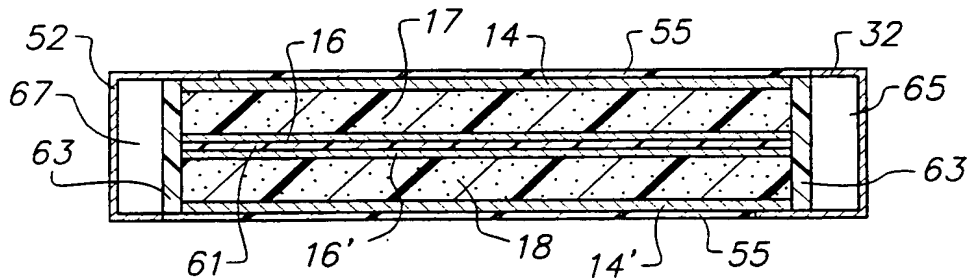


FIG. 15

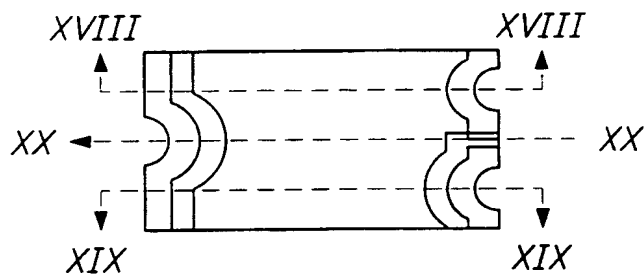


FIG. 16

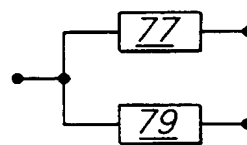


FIG. 17

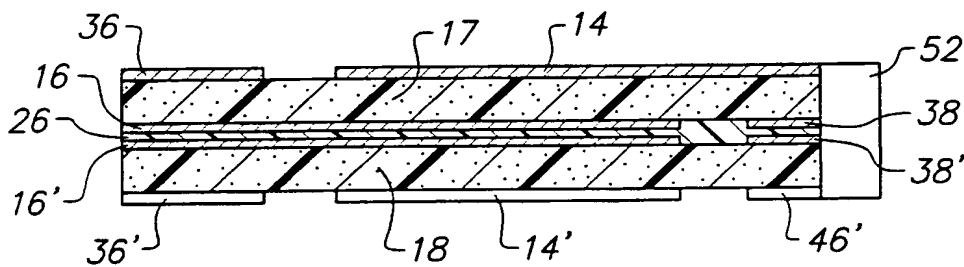


FIG. 18

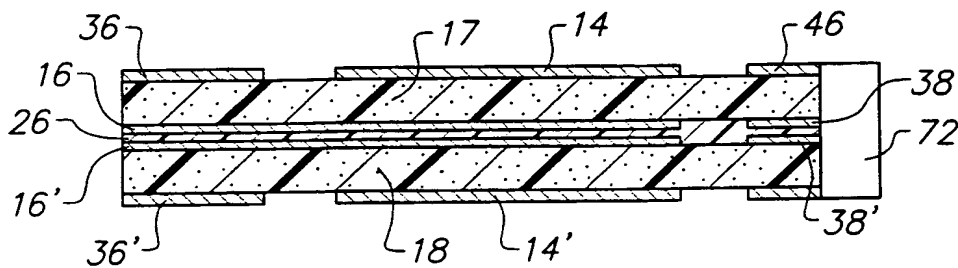


FIG. 19

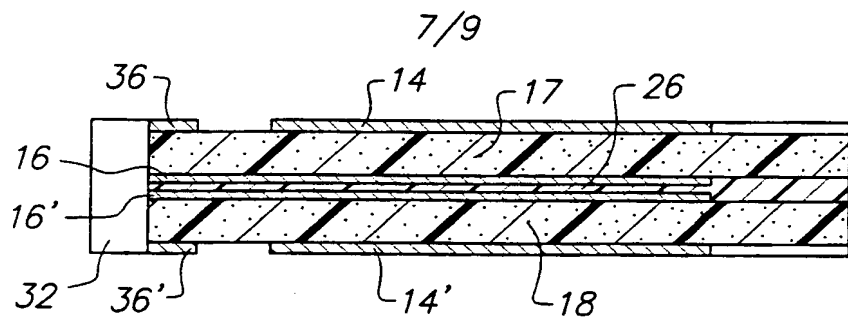


FIG. 20

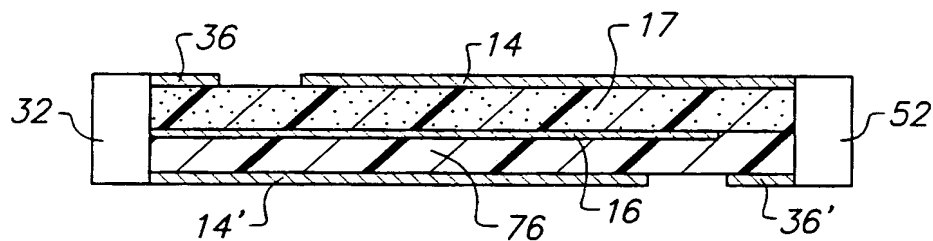


FIG. 21

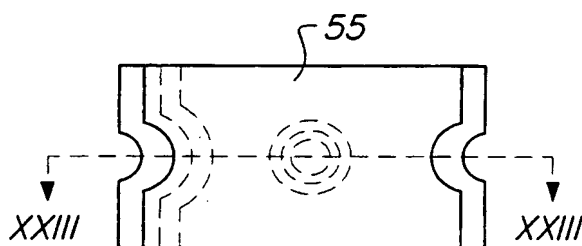


FIG. 22

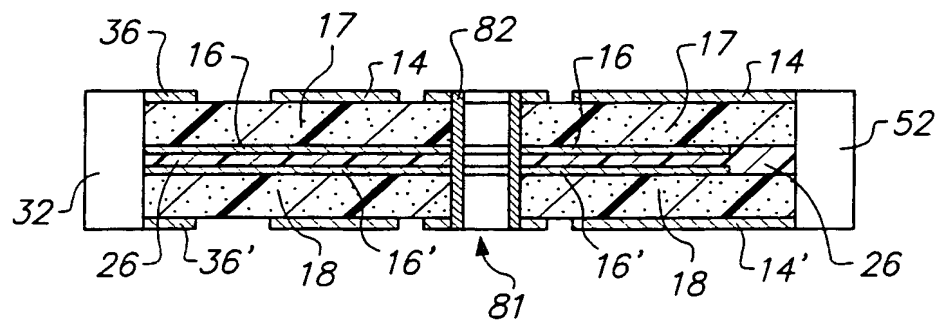
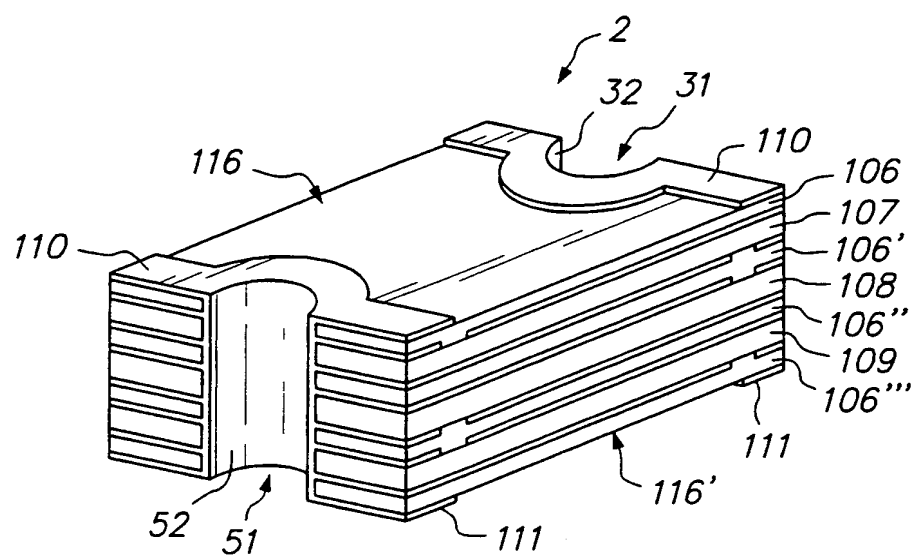
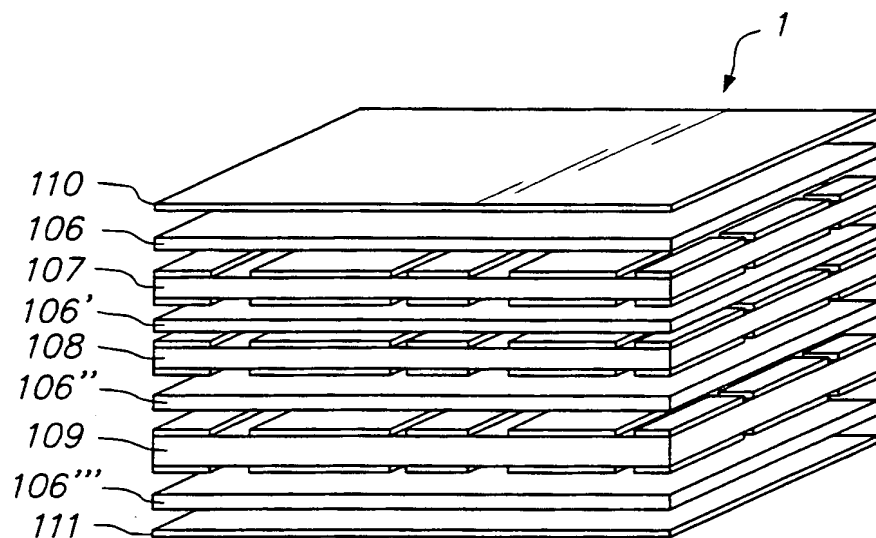


FIG. 23

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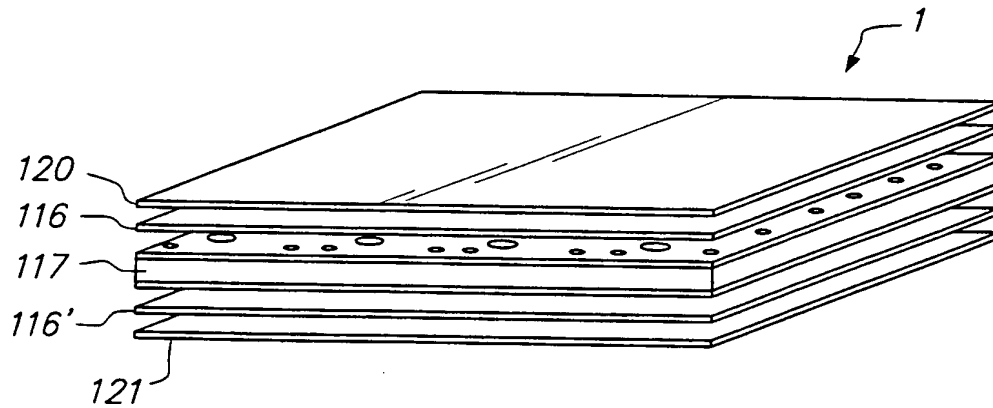


FIG. 26

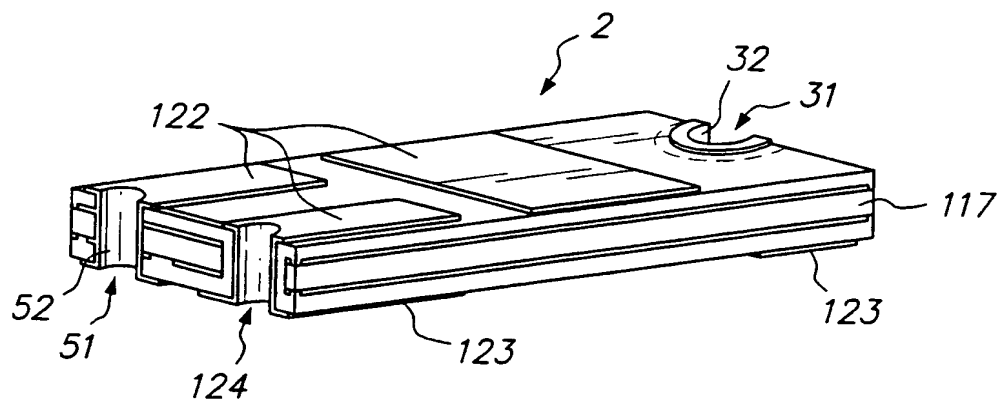


FIG. 27

TENT COOPERATION TREATY

PCT

INTERNATIONAL SEARCH REPORT

(PCT Article 18 and Rules 43 and 44)

Applicant's or agent's file reference MP1705-PCT	FOR FURTHER ACTION see Notification of Transmittal of International Search Report (Form PCT/ISA/220) as well as, where applicable, item 5 below.	
International application No. PCT/US 00/ 25118	International filing date (day/month/year) 13/09/2000	(Earliest) Priority Date (day/month/year) 14/09/1999
Applicant TYCO ELECTRONICS CORPORATION et al.		

This International Search Report has been prepared by this International Searching Authority and is transmitted to the applicant according to Article 18. A copy is being transmitted to the International Bureau.

This International Search Report consists of a total of 2 sheets.

☒ It is also accompanied by a copy of each prior art document cited in this report.

1. Basis of the report

a. With regard to the **language**, the international search was carried out on the basis of the international application in the language in which it was filed, unless otherwise indicated under this item.

☐ the international search was carried out on the basis of a translation of the international application furnished to this Authority (Rule 23.1(b)).

b. With regard to any **nucleotide and/or amino acid sequence** disclosed in the international application, the international search was carried out on the basis of the sequence listing :

☐ contained in the international application in written form.

☐ filed together with the international application in computer readable form.

☐ furnished subsequently to this Authority in written form.

☐ furnished subsequently to this Authority in computer readable form.

☐ the statement that the subsequently furnished written sequence listing does not go beyond the disclosure in the international application as filed has been furnished.

☐ the statement that the information recorded in computer readable form is identical to the written sequence listing has been furnished

2. ☐ **Certain claims were found unsearchable** (See Box I).

3. ☐ **Unity of invention is lacking** (see Box II).

4. With regard to the **title**,

☒ the text is approved as submitted by the applicant.

☐ the text has been established by this Authority to read as follows:

5. With regard to the **abstract**,

☒ the text is approved as submitted by the applicant.

☐ the text has been established, according to Rule 38.2(b), by this Authority as it appears in Box III. The applicant may, within one month from the date of mailing of this international search report, submit comments to this Authority.

6. The figure of the **drawings** to be published with the abstract is Figure No.

☐ as suggested by the applicant.

☒ because the applicant failed to suggest a figure.

☐ because this figure better characterizes the invention.

1

☐ None of the figures.

INTERNATIONAL SEARCH REPORT

National Application No
PCT/US 00/25118A. CLASSIFICATION OF SUBJECT MATTER
IPC 7 H01C7/02

According to International Patent Classification (IPC) or to both national classification and IPC

B. FIELDS SEARCHED

Minimum documentation searched (classification system followed by classification symbols)
IPC 7 H01C H01G

Documentation searched other than minimum documentation to the extent that such documents are included in the fields searched

Electronic data base consulted during the international search (name of data base and, where practical, search terms used)

EPO-Internal, PAJ, WPI Data

C. DOCUMENTS CONSIDERED TO BE RELEVANT

Category *	Citation of document, with indication, where appropriate, of the relevant passages	Relevant to claim No.
X	WO 99 45551 A (BOURNS MULTIFUSE HONG KONG LTD) 10 September 1999 (1999-09-10) the whole document	1-20
P, X	EP 0 955 643 A (MATSUSHITA ELECTRIC IND CO LTD) 10 November 1999 (1999-11-10) the whole document	1-20
X	& WO 98 29879 A (MATSUSHITA ELECTRIC IND CO LTD) 9 July 1998 (1998-07-09)	1-20

☐ Further documents are listed in the continuation of box C.☒ Patent family members are listed in annex.

* Special categories of cited documents:

- *A* document defining the general state of the art which is not considered to be of particular relevance
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- *L* document which may throw doubts on priority claim(s) or which is cited to establish the publication date of another citation or other special reason (as specified)
- *O* document referring to an oral disclosure, use, exhibition or other means
- *P* document published prior to the international filing date but later than the priority date claimed

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- *X* document of particular relevance; the claimed invention cannot be considered novel or cannot be considered to involve an inventive step when the document is taken alone
- *Y* document of particular relevance; the claimed invention cannot be considered to involve an inventive step when the document is combined with one or more other such documents, such combination being obvious to a person skilled in the art.
- *G* document member of the same patent family

Date of the actual completion of the international search

7 June 2001

Date of mailing of the international search report

13/06/2001

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Goossens, A

INTERNATIONAL SEARCH REPORT

Information on patent family members

International Application No

PCT/US 00/25118

Patent document cited in search report	Publication date	Patent family member(s)	Publication date
WO 9945551 A	10-09-1999	US 6172591 B EP 1060481 A US 2001000658 A	09-01-2001 20-12-2000 03-05-2001
EP 0955643 A	10-11-1999	US 6188308 B WO 9829879 A	13-02-2001 09-07-1998

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(19) World Intellectual Property Organization
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WO 01/20619 A3

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(22) International Filing Date:
13 September 2000 (13.09.2000)

(25) Filing Language: English

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(30) Priority Data:
09/395,869 14 September 1999 (14.09.1999) US

(63) Related by continuation (CON) or continuation-in-part (CIP) to earlier application:
US 09/395,869 (CIP)
Filed on 14 September 1999 (14.09.1999)

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(74) Agents: **GERSTNER, Marguerite, E.** et al.; Tyco Electronics Corporation, Intellectual Property Law Dept., 307 Constitution Drive, MS R20/2B, Menlo Park, CA 94025-1164 (US).

(81) Designated States (*national*): CN, JP, US.

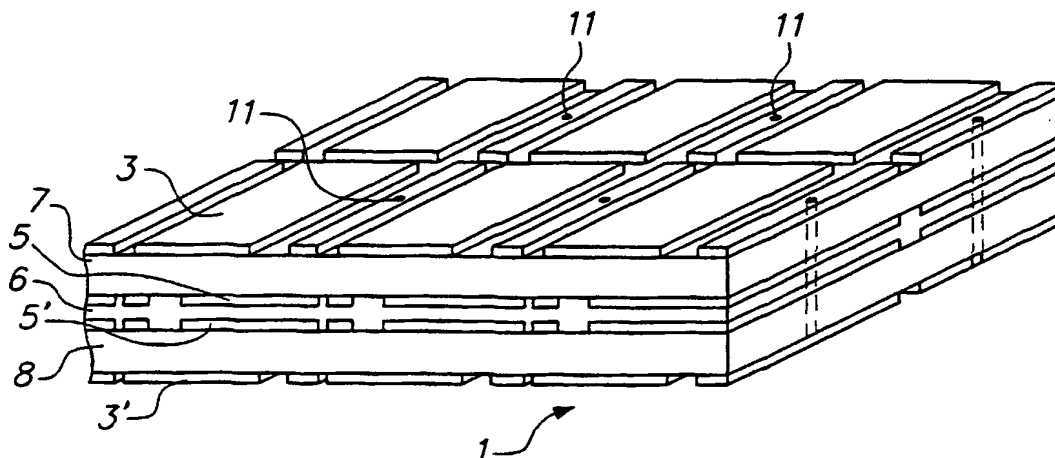
(84) Designated States (*regional*): European patent (AT, BE, CH, CY, DE, DK, ES, FI, FR, GB, GR, IE, IT, LU, MC, NL, PT, SE).

Published:
— with international search report

(88) Date of publication of the international search report:
6 December 2001

For two-letter codes and other abbreviations, refer to the "Guidance Notes on Codes and Abbreviations" appearing at the beginning of each regular issue of the PCT Gazette.

(54) Title: ELECTRICAL DEVICES AND PROCESS FOR MAKING SUCH DEVICES



(57) Abstract: A process for manufacturing a composite polymeric circuit protection device in which a polymeric assembly is provided and is then subdivided into individual devices (2). The assembly is made by providing first and second laminates (7, 8), each of which includes a laminar polymer element having at least one conductive surface, providing a pattern on at least one of the conductive surfaces on one laminate, securing the laminates in a stack (1), in a desired configuration, at least one conductive surface of at least one of the laminates forming an external conductive surface (3) of the stack, and making a plurality of electrical connections (31, 51) between a conductive surface of the first laminate and a conductive surface of the second laminate. The laminar polymer elements may be PTC conductive polymer compositions, so that the individual devices made by the process exhibit PTC behavior.

WO 01/20619 A3